TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX05F,TC74LCX05FN,TC74LCX05FT

Low-Voltage HEX Inverter with 5-V Tolerant Inputs and Outputs (open-drain)

Note: xxxFN (JEDEC SOP) is not available in Japan.

The TC74LCX05F/FN/FT is a high-performance CMOS inverter.

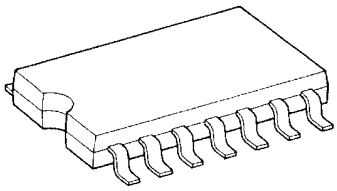
Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

Pin configuration and function are the same as the TC74LCX04, but the TC74LCX05F/FN/FT has high performance MOS N-channel transistor. (open-drain outputs)

The device is designed for low-voltage (3.3 V) V_{CC} applications, but it could be used to interface to 5-V supply environment for inputs.

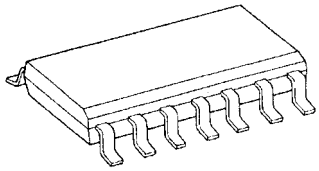
All inputs are equipped with protection circuits against static discharge.

TC74LCX05F



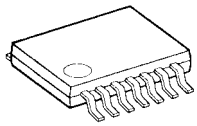
SOP14-P-300-1.27

TC74LCX05FN



SOL14-P-150-1.27

TC74LCX05FT



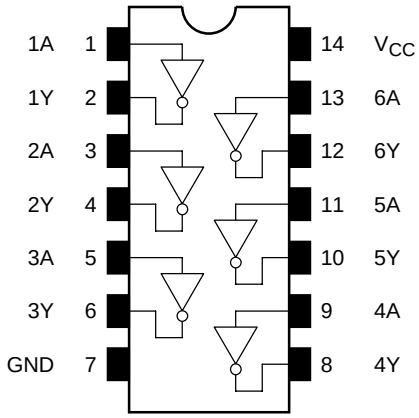
TSSOP14-P-0044-0.65

Weight
SOP14-P-300-1.27: 0.18 g (typ.)
SOL14-P-150-1.27: 0.12 g (typ.)
TSSOP14-P-0044-0.65: 0.06 g (typ.)

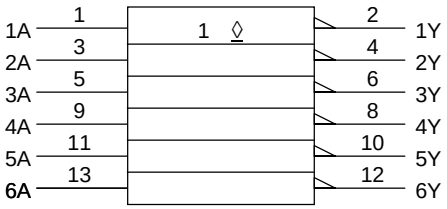
Features

- Low-voltage operation: V_{CC} = 2.0 to 3.6 V
- High-speed operation: t_{pz} = 5.0 ns (max) (V_{CC} = 3.0 to 3.6 V)
- Output current: I_{OL} = 24 mA (min) (V_{CC} = 3.0 V)
- Latch-up performance: −500 mA
- Available in JEDEC SOP, JEITA SOP and TSSOP
- Open-drain outputs
- Power-down protection is provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 05 type

Pin Assignment (top view)



IEC Logic Symbol

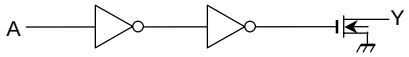


Truth Table

Inputs	Outputs
A	Y
L	Z
H	L

Z: High impedance

System Diagram (per gate)



Maximum Ratings

Characteristics	Symbol	Rating	Unit
Power supply voltage	V_{CC}	−0.5 to 7.0	V
DC input voltage	V_{IN}	−0.5 to 7.0	V
DC output voltage	V_{OUT}	−0.5 to 7.0 (Note 1) −0.5 to V_{CC} + 0.5 (Note 2)	V
Input diode current	I_{IK}	−50	mA
Output diode current	I_{OK}	−50 (Note 3)	mA
DC output current	I_{OUT}	50	mA
Power dissipation	P_D	180	mW
DC V_{CC}/ground current	I_{CC}/I_{GND}	±100	mA
Storage temperature	T_{stg}	−65 to 150	°C

Note 1: Output in OFF state

Note 2: Low state. I_{OUT} absolute maximum rating must be observed.

Note 3: $V_{OUT} < GND$

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Power supply voltage	V_{CC}	2.0 to 3.6	V
		1.5 to 3.6 (Note 4)	
Input voltage	V_{IN}	0 to 5.5	V
Output voltage	V_{OUT}	0 to 5.5 (Note 5)	V
		0 to V_{CC} (Note 6)	
Output current	I_{OH}/I_{OL}	24 (Note 7)	mA
		12 (Note 8)	
Operating temperature	T_{opr}	−40 to 85	°C
Input rise and fall time	dt/dv	0 to 10 (Note 9)	ns/V

Note 4: Data retention only

Note 5: Output in OFF state

Note 6: Low state

Note 7: V_{CC} = 3.0 to 3.6 V

Note 8: V_{CC} = 2.7 to 3.0 V

Note 9: V_{IN} = 0.8 to 2.0 V, V_{CC} = 3.0 V

Electrical Characteristics

DC Characteristics (Ta = –40 to 85°C)

Characteristics		Symbol	Test Condition		V_{CC} (V)	Min	Max	Unit
Input voltage	H-level	V_{IH}	—		2.7 to 3.6	2.0	—	V
	L-level	V_{IL}	—		2.7 to 3.6	—	0.8	
Output voltage	L-level	V_{OL}	V_{IN} = V_{IH}	I_{OL} = 100 μA	2.7 to 3.6	—	0.2	V
				I_{OL} = 12 mA	2.7	—	0.4	
				I_{OL} = 16 mA	3.0	—	0.4	
				I_{OL} = 24 mA	3.0	—	0.55	
Input leakage current		I_{IN}	V_{IN} = 0 to 5.5 V		2.7 to 3.6	—	±5.0	μA
Output OFF state current		I_{OZ}	V_{IN} = V_{IL}, V_{OUT} = 0 to 5.5 V		2.7 to 3.6	—	±5.0	μA
Power-off leakage current		I_{OFF}	V_{IN}/V_{OUT} = 5.5 V		0	—	10.0	μA
Quiescent supply current		I_{CC}	V_{IN} = V_{CC} or GND		2.7 to 3.6	—	10.0	μA
			V_{IN}/V_{OUT} = 3.6 to 5.5 V		2.7 to 3.6	—	±10.0	
Increase in Icc per input		ΔI_{CC}	V_{IH} = V_{CC} − 0.6 V		2.7 to 3.6	—	500	

AC Characteristics (Ta = –40 to 85°C)

Characteristics	Symbol	Test Condition	V_{CC} (V)	Min	Max	Unit
Output enable time	t_{pZL}	Figure 1, Figure 2	2.7	1.0	6.0	ns
			3.3 ± 0.3	0.8	5.0	
Output disable time	t_{pLZ}	Figure 1, Figure 2	2.7	1.0	6.0	ns
			3.3 ± 0.3	0.8	5.0	
Output to output skew	t_{osZL}	(Note 10)	2.7	—	—	ns
			3.3 ± 0.3	—	1.0	

Note 10: Parameter guaranteed by design.
($t_{osZL} = |t_{pZLm} - t_{pZLn}|$)

Dynamic Switching Characteristics
(Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500\ \Omega$)

Characteristics	Symbol	Test Condition	V_{CC} (V)	Typ.	Unit
Quiet output maximum dynamic V_{OL}	V_{OLP}	V_{IH} = 3.3 V, V_{IL} = 0 V	3.3	0.8	V
Quiet output minimum dynamic V_{OL}	$\|V_{OLV}\|$	V_{IH} = 3.3 V, V_{IL} = 0 V	3.3	0.8	V

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V_{CC} (V)	Typ.	Unit
Input capacitance	C_{IN}	—	3.3	7	pF
Output capacitance	C_{OUT}		3.3	8	pF
Power dissipation capacitance	C_{PD}	f_{IN} = 10 MHz (Note 11)	3.3	5	pF

Note 11: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.
Average operating current can be obtained by the equation:
I_{CC} (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6$ (per gate)

AC Test Circuit

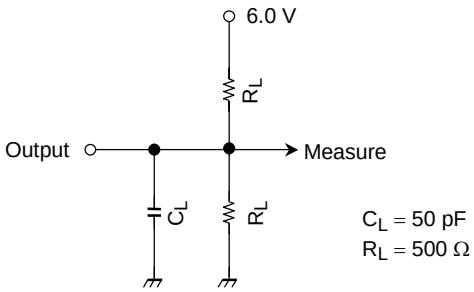


Figure 1

AC Waveform

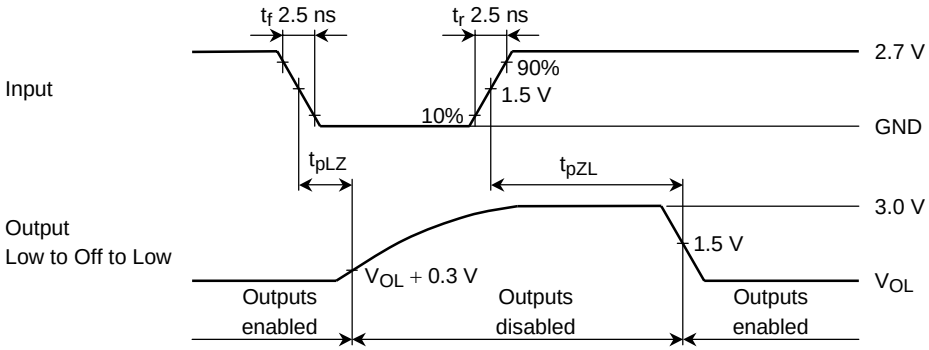
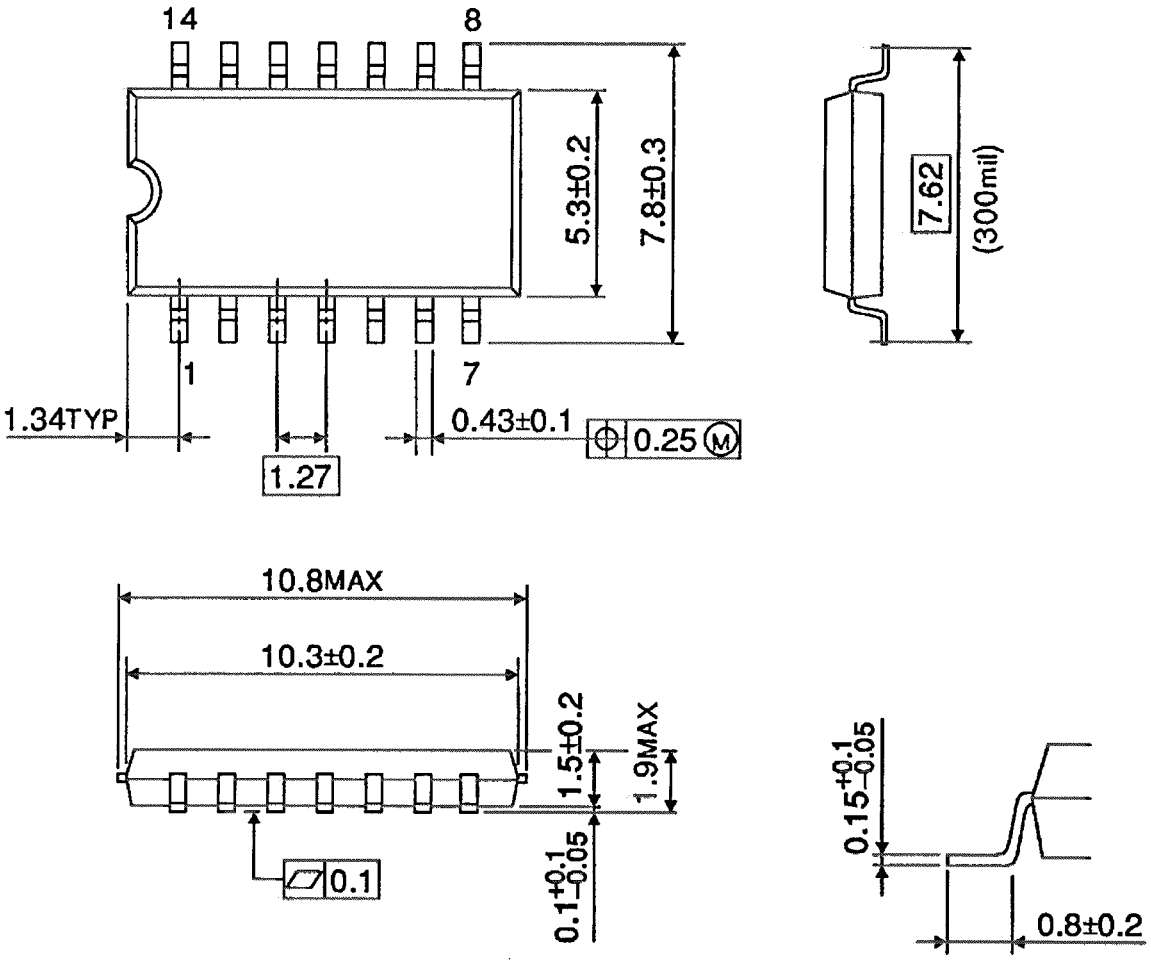


Figure 2 t_{pLZ}, t_{pZL}

Package Dimensions

SOP14-P-300-1.27 Unit : mm



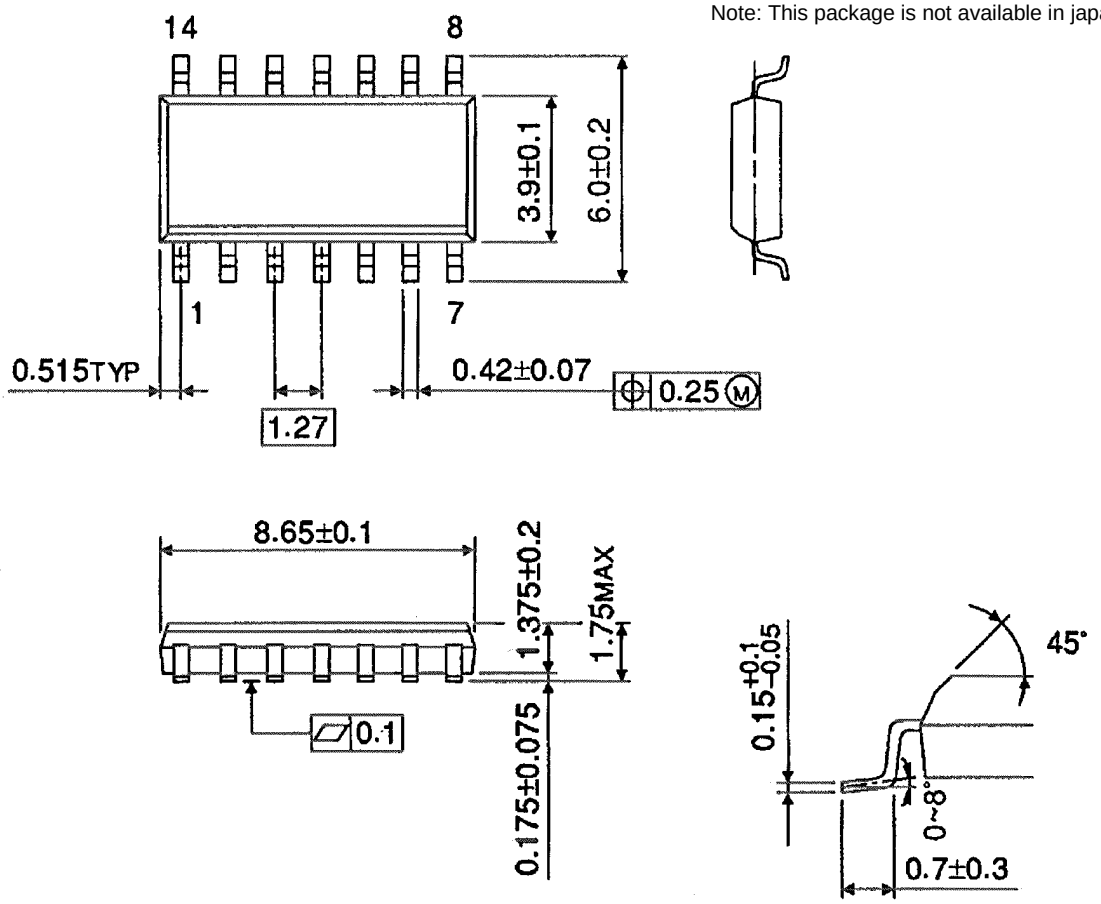
Weight: 0.18 g (typ.)

Package Dimensions

SOL14-P-150-1.27

Unit : mm

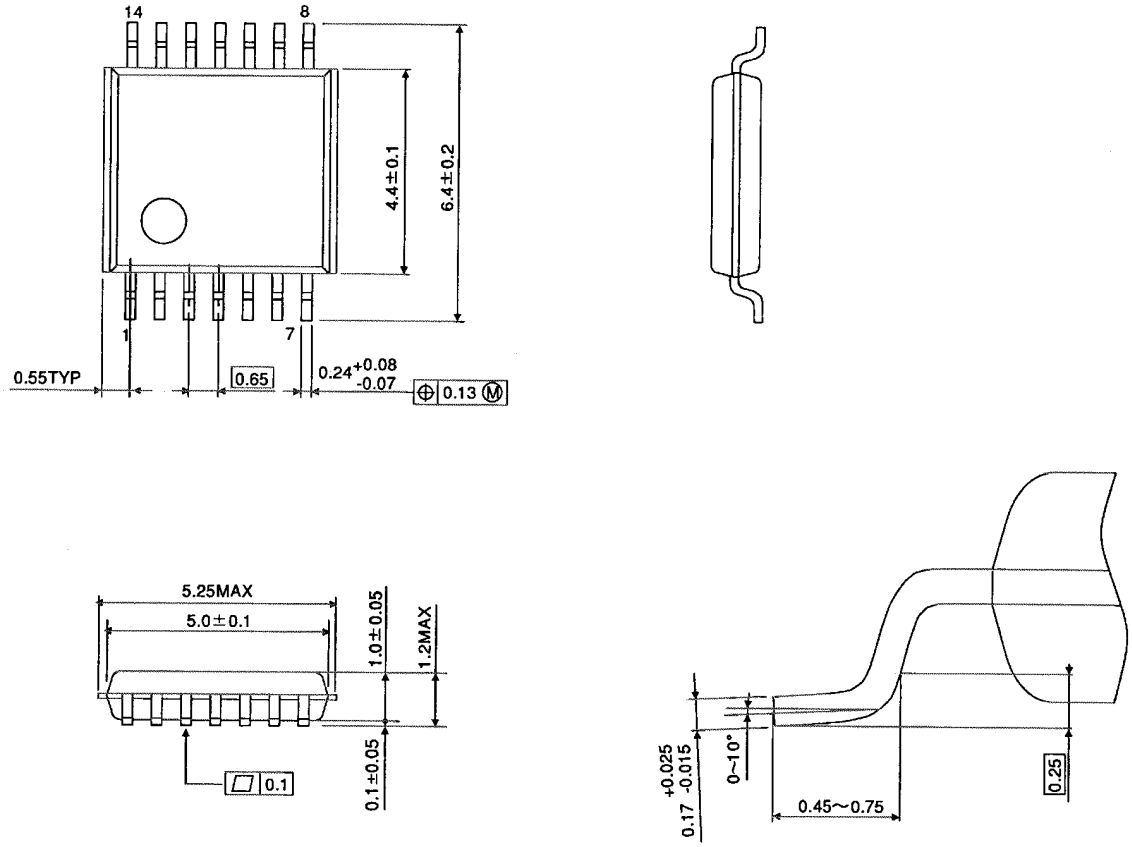
Note: This package is not available in japan.



Weight: 0.12 g (typ.)

Package Dimensions

TSSOP14-P-0044-0.65 Unit : mm



Weight: 0.06 g (typ.)

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